

Development of new photon detection device for Cherenkov and fluorescence radiation

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Abstract. Recent progress on the development of a new solid state detector allowed the use of finely pixelled photocathodes obtained from silicon semiconductors. SiPM detectors seem to be an ideal tool for the detection of Cherenkov and fluorescence light in spite of their not yet resolved criticism for operating temperature and intrinsic noise. The main disadvantage of SiPM in this case is the poor sensitivity in the wavelength range 300-400 nm, where the Cherenkov light and fluorescence radiation are generated. We report on the possibility to realize a new kind of pixelled photodetector based on the use of silicon substrate with carbon nanotube compounds, more sensitive to the near UV radiation. Also if at the very beginning, the development of such detector appears very promising and useful for astroparticle physics, both in the ground based arrays and in the space experiments. The detectors are ready to be operated in conditions of measurements without signal amplification.

1. INTRODUCTION

Multiwall carbon nanotubes (MWCNTs) consist of multiple layers of graphite sheets forming concentric cylinders, from two to many tens. Some MWCNT properties made them an attractive material for UV photodetector [1, 2] and photosensor applications, when associated their response to light stimulus. We present a novel large area photodetector featuring low noise, good efficiency in the wavelength range from 200 to 1000 nm and great surface uniformity. This detector has been obtained by coupling the optoelectronic characteristics of carbon nanostructures (CNs) with the well-known optoelectronic properties of silicon. The proposed device mimics the behaviour of a p-n-p phototransistor where the carbon nanostructure/silicon interface acts as the base, while the emitter is the Schottky

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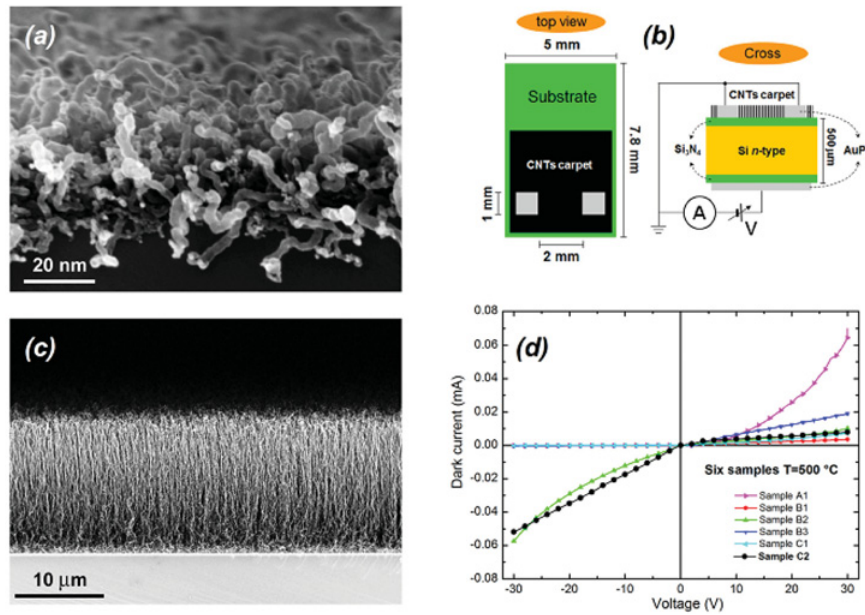


Figure 1. SEM images of CN grown at a temperature of 500 °C (a) and 700 °C (c) on the substrate schematized in (b). See the morphological differences between carbon nanofibers (a) and the MWCNTs (c), induced by the growth temperature. (d), Dark current-voltage characteristic of six Si-CN devices.

Metal-Insulator-Semiconductor junction between the back electrode and the silicon substrate and the collector is the CN layer itself.

2. CN-SI BASED DEVICE

Figure 1a shows a SEM image of CN samples grown at a CVD temperature of 500 °C, which behaves as bunches of ropes of carbon nanostructures with variable diameter along the tubes. By using a higher CVD growing temperature, 700 °C, (Fig. 1c) vertically aligned carbon nanotubes are obtained, with an average diameter of 19 nm and a length greater than 16 microns.

Figure 1c shows the schematic of the geometry of the device. CNs are grown in the black area around electrodes. As silicon substrate we used 500 mm thick n-doped silicon substrates $5 \times 7.8 \text{ mm}^2$ sized with an average carrier concentration of 10^{14} cm^{-3} , covered on both its faces by a 140 nm silicon nitride (Si_3N_4) layer. The drain voltage is applied between the electrodes of the top side and the electrode on the back (Fig. 1b) of the substrate. We first analysed the behaviour of six 500 °C, whose dark current is reported in Fig. 1d. Most of them behave like a Schottky junction.

Figure 2a shows the photocurrent measured when the sample is illuminated with a 785 nm continuous laser beam at various light power intensities. The plot in Fig. 2a confirms the rectifying behaviour also in the presence of high intensity of illumination and in a large range of drain voltage. This is due to the particular structure of substrate in which the electrode on the back forms a Schottky MIS junction with the silicon substrate, whereas CNs form a carbon nanostructure-silicon heterojunction [3].

The device operates as a p-n-p phototransistor. As the applied voltage increases, the current is null until one reaches a few Volts threshold, then it increases linearly until saturation is reached. The breakdown limit has been observed well beyond a drain voltage of -100 V . On the other hand, as the light power intensity increases, a higher drain voltage is necessary to drain all the charges generated

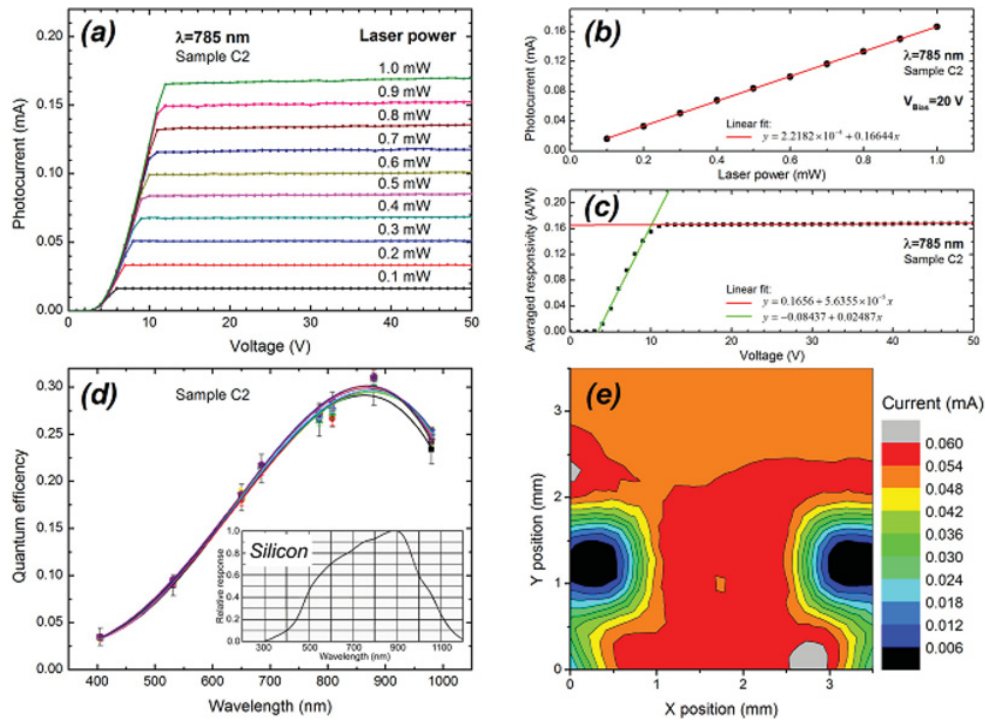


Figure 2. a) I-V curve at $\lambda = 785$ nm for various laser light intensities. b) Linearity plot at $\lambda = 785$ nm for an applied drain voltage of 20 V. c) Averaged responsivity at $\lambda = 785$ nm. In the plot linear fit curves are superimposed fitting the signal rise and the current plateau. d) Quantum efficiency (QE) as a function of the wavelength ranging from 405 to 980 nm fitted with a four order polynomial curve. In the inset the spectral response of silicon. e) Detector surface photocurrent map demonstrating the uniformity of sensitive area covered by CNs.

and reach the current saturation. It must be noted that no electronic amplification has been applied: the measured current is the one directly generated by photons impacting the CN layer.

3. THE MWCNT-SI BASED DEVICE

The same measurements have been repeated for the devices based on vertically aligned multiwall carbon nanotubes (Fig. 1c). The photosensitivity of the junction is shown in Fig. 3a reporting the I-V plot for device dark current and in Fig. 3b reporting the photocurrent as a function of the drain voltage obtained using a 650 nm laser diode light at various power intensities. These I-V curves, as well as the average responsivity curve, show the same behaviour of the CN-based device both as a function of the drain voltage and the laser light power intensity. However, the junction threshold increases significantly for MWCNTs reaching a value of 6.55 V (Fig. 3c), well higher than the 3.55 V thresholds measured for CNs (Fig. 2c). Moreover, the correlation between the detector quantum efficiency QE and the radiation wavelength is shown in Fig. 3d, fitted with a polynomial curve (blue curve) and compared with that of CN-based device. In this case, the QE is four times higher than that measured for CN-based device in the UV energy region. The present result paves the road to the use of MWCNT-based detectors in the UV region.

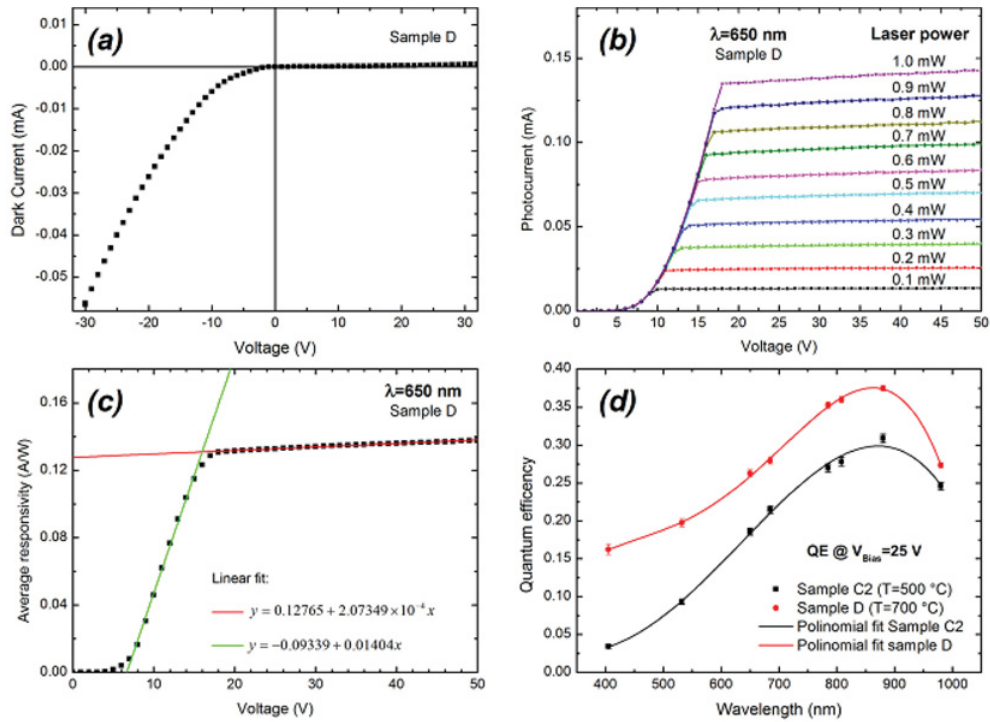


Figure 3. a) Dark current. b) I-V curve at $\lambda = 650$ nm for various laser light intensities. c) Averaged responsivity at $\lambda = 650$ nm. In the curves are superimposed fitting the signal rise and the current plateau. d) QE as a function of the wavelength ranging from 405 to 980 nm of detector @ 500 °C compared with that @ 700 °C fitted with a four order polynomial function.

4. DISCUSSION

The devices used for these measurements, in the absence of carbon nanostructures grown on their surface, show a backside MIS (Metal-Insulator-Semiconductor) junction between the n-doped silicon substrate and the metallic electrode [4]. An identical MIS junction between the silicon and the electrodes is present on the upper surface. Therefore, in the absence of CNTs the device can be schematized as a double Schottky junction polarized back-to-back (Fig. 4a). The presence of the CNTs overbalances the system: a heterojunction is created between the CNT layer and the silicon (Fig. 4b). The presence of the insulator (silicon nitride) is bypassed by the formation of permanent low resistivity conduction channels inside this film, favouring the charge flow through the silicon substrate. The channels are created as an external voltage is applied to the device thanks to the peculiar characteristics of CNTs acting as one-dimensional conductive elements. In addition CNTs are semi-transparent to the radiation.

Thus light addressed on the CNT layer partially is converted in e-h pairs, partially reaches the silicon substrate in the depletion area under the CNT layer producing additional e-h pairs. The photo generated electrons or holes can migrate towards the CNT layer or can cross the junction from CNT to the silicon thanks to the presence of such low resistive ducts. The enhanced QE in the UV wavelength region is mainly due to photons converted inside the CNT layer while in the visible-near IR region light converts essentially inside the CNTs.

For this reason the device shows the characteristics of a p-n-p photo-transistor, where the metallic contact on the back acts as the emitter, the upper CNT layer is the collector and the CNT-silicon depletion area plays the role of the transistor base [5].

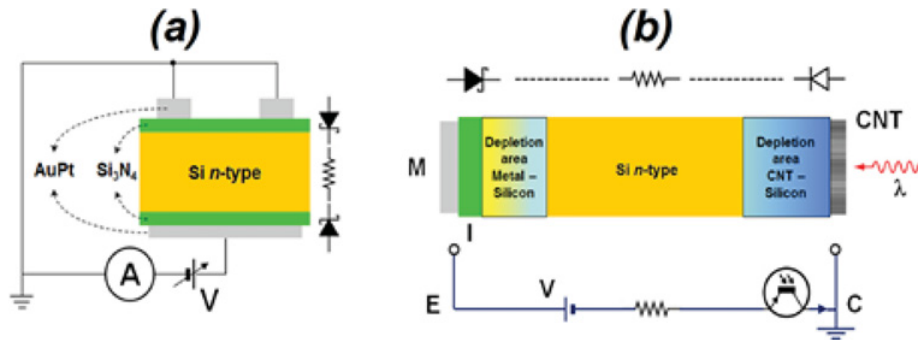


Figure 4. Layout of CNT-Si with the junctions. a) In absence of CNTs the device can be schematized as a double Schottky junction polarized back-to-back. b) The presence of the CNTs overbalances the system: a heterojunction is created between the CNT layer and the silicon.

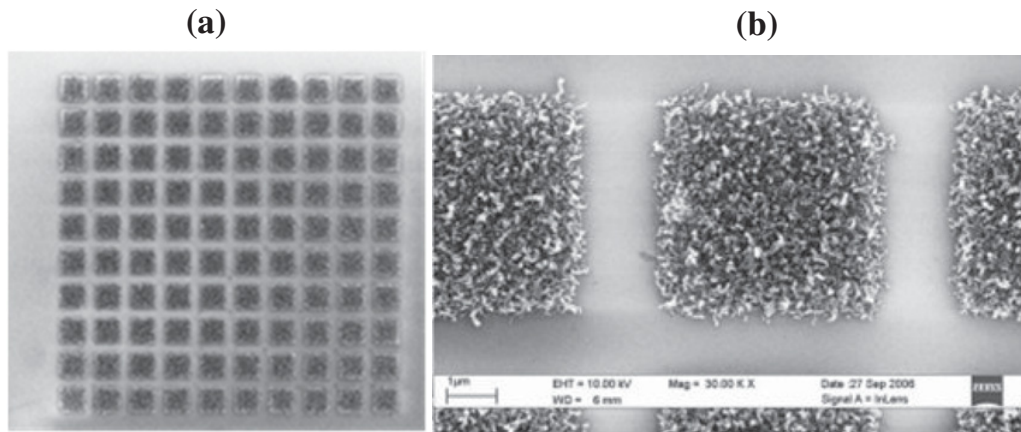


Figure 5. a) Pixels of $4 \times 4 \mu\text{m}^2$ each obtained nanolithography. b) Individual CNT pixel.

5. CONCLUSION

The device is extremely promising in a future use in high energy physic experiments and medical applications. Highly pixelled surfaces can be obtained by means of micro or nano lithography (Fig. 5) [6] permitting the coupling of detector with external lighting devices such as scintillators and optical fibres. Our procedure to grow the carbon nanotubes with a definite pattern is based on a lift-off process. “Lift-off” is a method for making metallic patterns on a substrate, especially for those noble-metal thin films such as platinum, tantalum, nickel or iron, which are difficult to be etched by conventional methods [6]. For our device the dimension of pattern spreads from $10 \mu\text{m}$ down to 100 nm . This is obtained with an Electron Beam Lithography system. Figure 5a reports the results obtained with the combined use of nanolithography and lift-off process: a square matrix of 10×10 pixels, $4 \times 4 \mu\text{m}^2$ each. What is important is that nanotubes grow only where the lift-off process left the catalyst: no carbon compounds appear to be present outside the pixel (Fig. 5b).

Pixelled large area photocathode can be used for the medical imaging or to manufacture fluorescence light detectors or to detect Cherenkov light cone with high accuracy. Progress on this line needs the cooperation of industry to develop single photon device by creating in the silicon base a secondary p-n junction acting as signal amplifier.

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