

## Article

# Single Step 2-Port Device De-Embedding Algorithm for Fixture-DUT-Fixture Network Assembly

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**Abstract:** The de-embedding of measurement fixtures is relevant for an accurate experimental characterization of radio frequency and digital electronic devices. The standard technique consists in removing the effects of the measurement fixtures by the calculation of the transfer scattering parameters (T-parameters) from the available measured (or simulated) global scattering parameters (S-parameters). The standard de-embedding is achieved by a multiple steps process, involving the S-to-T and subsequent T-to-S parameter conversion. In a typical measurement setup, two fixtures are usually placed before and after the device under test (DUT) allowing the connection of the device to the calibrated vector network analyzer coaxial ports. An alternative method is proposed in this paper: it is based on the newly developed multi-network cascading algorithm. The matrices involved in the fixture-DUT-fixture cascading gives rise to a non-linear set of equations that is in one step analytically solved in closed form, obtaining a unique solution. The method is shown to be effective and at least as accurate as the standard multi-step de-embedding one.

**Keywords:** S-parameters; de-embedding; 2-port networks; measurement setup



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## 1. Introduction

The precise characterization and validation of electronics devices for radio frequency (RF) and high-speed digital applications relies on scattering parameters (S-parameters) measurements. A well-known preliminary step for an accurate measurement is the calibration of the vector network analyzer (VNA) which moves the reference planes from the VNA output ports (usually its connectors) to the end of the cables used for the VNA to the device under test (DUT) connection [1,2]. This procedure is sufficient as long as the DUT that needs to be measured can be accessed by the VNA cables through coaxial connectors that directly match the VNA cables' impedance [3]. However, very often such coaxial access points are not directly available for complex devices, unless appropriate test fixtures are designed to make accessible the DUT through coaxial ports. This is the case concerning the characterization of on-wafer devices, integrated circuits and packages, single transistors, connectors for RF and high-speed digital applications, waveguide transitions and other complex structures [4]. In this case the de-embedding of the test fixtures (usually named "left" and "right" fixtures) need to be applied in order to extract the impact of their presence from the overall measured S-parameter. This is done knowing the S-parameter data of the single fixtures that are applied before and after the DUT [5–9]. Usually the well-known de-embedding process in frequency domain is based at least on three main conceptual steps: first, the conversion of the S-parameters in the transfer scattering parameters (T-parameters) [10], second the algebraic manipulation and inversion of the T-parameter matrices, and third the final T-to-S parameter transformation. It should be noted that when

actually implemented each single step calls some others making the real implementation rather complex.

In this paper a sophisticated but more efficient single step de-embedding process is proposed to perform the de-embedding of both left and right fixtures of a 2-port system in only a single step through the solution of a linear system of equations, without the need for any matrix inversion involved in the aforementioned S-to-T and T-to-S parameter conversion that introduces the numerical error (typically  $O(N^2)$  where  $N$  is the order of the matrix) due to the iterative process for the evaluation of the inverse [11]. Once implemented, the solution is applied directly on the raw measured S-parameters of the fixture-DUT-fixture system knowing those of each fixture without any conversion to T-parameters. The analytical solution is an extension of the algorithm developed for the cascading of two S-parameter blocks in [12]. The algorithm is expanded to the case of three blocks (or networks) representing the S-parameters of the left fixture (FL), of the DUT and of the right fixture (FR) based on the cascading of the S-parameter blocks network considered in [12]. The classical method for the cascading of two networks is here expanded to the case of three networks that are identified in the following as the left fixture (FL), the DUT, and the right fixture (FR). The direct solution of the three blocks cascading process gives rise, through few matrix-algebraic manipulations, to a set of four non-linear equations in four auxiliary unknowns that are directly and analytically related to the sought DUT S-parameters ( $S_{DUT1,1}, S_{DUT1,2}, S_{DUT2,1}, S_{DUT2,2}$ ). This system of non-linear equations has a closed form solution clear of any numerical error.

It is used for building a linear set of four equations in five unknowns: the four unknowns of the DUT S-parameters ( $S_{DUT1,1}, S_{DUT1,2}, S_{DUT2,1}, S_{DUT2,2}$ ) plus their combination coming out from the matrix determinant built along the cascading process. By means of a matrix-algebraic manipulation the problem is reduced to a set of four auxiliary equations in four unknowns and their solution used to find the valid and unique closed form solution of the DUT S-parameters.

The structure of the paper is the following: the S-parameter cascading of two two-port networks is briefly reviewed in Section 2, and it is expanded for the case of three two-port networks. Section 3 presents the analytical derivation of the method for the single-step fixture de-embedding. Section 4 provides some validation examples by comparing the original DUT S-parameters with those computed after applying the de-embedding developed here. Section 5 offers some concluding remarks.

## 2. S-Parameter Cascade of Three Networks

### 2.1. Review of Two-Network S-Parameter Cascading Algorithm

The two 2-port S-parameter networks FL and FR in Figure 1 are considered, whose S-parameters are defined by (1) and (2), respectively, and they are assumed to be known.

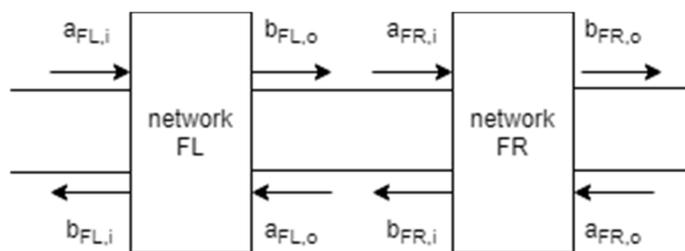
$$\begin{bmatrix} b_{FL,i} \\ b_{FL,o} \end{bmatrix} = \begin{bmatrix} S_{FL\ 1,1} & S_{FL\ 1,2} \\ S_{FL\ 2,1} & S_{FL\ 2,2} \end{bmatrix} \begin{bmatrix} a_{FL,i} \\ a_{FL,o} \end{bmatrix} \tag{1}$$

$$\begin{bmatrix} b_{FR,i} \\ b_{FR,o} \end{bmatrix} = \begin{bmatrix} S_{FR\ 1,1} & S_{FR\ 1,2} \\ S_{FR\ 2,1} & S_{FR\ 2,2} \end{bmatrix} \begin{bmatrix} a_{FR,i} \\ a_{FR,o} \end{bmatrix} \tag{2}$$

where  $a_{\alpha,\beta}$  and  $b_{\alpha,\beta}$  are the incident and reflected power waves respectively [10]. The subscript  $\alpha = FL, FR$  denotes if the wave belongs to the FL or FR network; the subscript  $\beta = i,o$  identifies if the wave is at the input or output port of the network.

An overall S-parameters matrix that combines all elements of both networks can be written as:

$$\begin{bmatrix} b_{FL,i} \\ b_{FR,o} \\ b_{FL,o} \\ b_{FR,i} \end{bmatrix} = \begin{bmatrix} S_{FL\ 1,1} & 0 & 0 & S_{FL\ 1,2} \\ 0 & S_{FR\ 2,2} & S_{FR\ 2,1} & 0 \\ S_{FL\ 2,1} & 0 & 0 & S_{FL\ 2,2} \\ 0 & S_{FR\ 1,2} & S_{FR\ 1,1} & 0 \end{bmatrix} \begin{bmatrix} a_{FL,i} \\ a_{FR,o} \\ a_{FR,i} \\ a_{FL,o} \end{bmatrix} \tag{3}$$



**Figure 1.** Cascade of two 2-port S-parameter networks: “FL” stands for left feature network and “FR” stands for right feature network.

A compact form of the vectors and matrix in (3) can be defined by naming *external parameters* (*e*) the input waves of network FL and the output waves of networks FR, and *internal parameters* (*i*) the output waves of network FL and the input waves of network FR, according to the order in which each single S-parameter is placed in the matrix in (3).

$$\bar{b}_e = \begin{bmatrix} b_{FL,i} \\ b_{FR,o} \end{bmatrix} \tag{4a}$$

$$\bar{b}_i = \begin{bmatrix} b_{FL,o} \\ b_{FR,i} \end{bmatrix} \tag{4b}$$

$$\bar{a}_e = \begin{bmatrix} a_{FL,i} \\ a_{FR,o} \end{bmatrix} \tag{4c}$$

$$\bar{a}_i = \begin{bmatrix} a_{FR,i} \\ a_{FL,o} \end{bmatrix} \tag{4d}$$

This leads us to write (3) as in (4e):

$$\begin{bmatrix} \bar{b}_e \\ \bar{b}_i \end{bmatrix} = \begin{bmatrix} \bar{S}_{ee} & \bar{S}_{ei} \\ \bar{S}_{ie} & \bar{S}_{ii} \end{bmatrix} \begin{bmatrix} \bar{a}_e \\ \bar{a}_i \end{bmatrix} \tag{4e}$$

In (4e) and in the following the vector and matrix variables will be denoted by one ( $\bar{b}$ ) or two ( $\bar{S}$ ) hats, respectively.

At the boundary between the two networks, the waves must be continuous. In other words, the condition (5) applies:

$$\bar{b}_i = \bar{a}_i \tag{5}$$

By enforcing this continuity condition, one can obtain the expression of the total scattering parameters  $S_{TOT}$  in (6) from the external end ports FL<sub>i</sub> and FR<sub>o</sub> of the cascaded FL and FR networks.

$$\bar{S}_{TOT} = \bar{S}_{ee} + \bar{S}_{ei} \left( \bar{I}_d - \bar{S}_{ii} \right)^{-1} \bar{S}_{ie} \tag{6}$$

where  $\bar{I}_d$  is the identity matrix with the same size of  $\bar{S}_{ii}$ .

### 2.2. Single Step S-Parameter Cascading Algorithm for Three 2-Port Networks

The process described above in (1)–(6) is extended to the case of interest based on the three 2-port networks in Figure 2. Since this configuration is preparatory for the de-embedding technique, the three networks are identified as FL, FR and DUT placed in between.

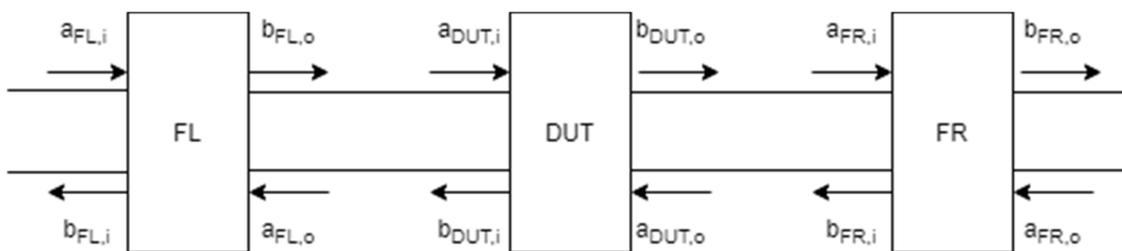


Figure 2. System of three 2-port S-parameter networks to be cascaded.

The S-parameters associated to each of these networks are given by (1) for FL, by (2) for FR and by (7) for the DUT.

$$\begin{bmatrix} b_{DUT,i} \\ b_{DUT,o} \end{bmatrix} = \begin{bmatrix} S_{DUT\ 1,1} & S_{DUT\ 1,2} \\ S_{DUT\ 2,1} & S_{DUT\ 2,2} \end{bmatrix} \begin{bmatrix} a_{DUT,i} \\ a_{DUT,o} \end{bmatrix} \tag{7}$$

As done in (3) for the simple case of two networks, the S-parameters of the three networks in Figure 2 can be arranged into a single matrix. This is done in (8) where the four S-parameter elements of the three networks are arranged such that the same boundary conditions in (5) apply.

$$\begin{bmatrix} b_{FL,i} \\ b_{FR,o} \\ b_{FL,o} \\ b_{DUT,i} \\ b_{DUT,o} \\ b_{FR,i} \end{bmatrix} = \begin{bmatrix} S_{FL\ 1,1} & 0 & 0 & S_{FL\ 1,2} & 0 & 0 \\ 0 & S_{FR\ 2,2} & 0 & 0 & S_{FR\ 2,1} & 0 \\ S_{FL\ 2,1} & 0 & 0 & S_{FL\ 2,2} & 0 & 0 \\ 0 & 0 & S_{DUT\ 1,1} & 0 & 0 & S_{DUT\ 1,2} \\ 0 & 0 & S_{DUT\ 2,1} & 0 & 0 & S_{DUT\ 2,2} \\ 0 & S_{FR\ 1,2} & 0 & 0 & S_{FR\ 1,1} & 0 \end{bmatrix} \begin{bmatrix} a_{FL,i} \\ a_{FR,o} \\ a_{DUT,i} \\ a_{FL,o} \\ a_{FR,i} \\ a_{DUT,o} \end{bmatrix} \tag{8}$$

In order to map (8) into (4), the size of the sub-matrices defined in (4) will be the following:  $2 \times 2$  for the  $\bar{S}_{e,e}$ ,  $2 \times 4$  for the  $\bar{S}_{e,i}$ ,  $4 \times 2$  for the  $\bar{S}_{i,e}$ , and  $4 \times 4$  for the  $\bar{S}_{i,i}$ . This matrix arrangement will make possible a direct calculation of the cascaded S-parameters according to (6), with  $\bar{S}_{TOT}$  representing the S-parameters between the external end ports FL<sub>i</sub> and FR<sub>o</sub> in Figure 2.

### 3. Single-Step Algorithm for Device under Test (DUT) De-Embedding

The S-parameter matrix equation in (6) is written in different form as in (9):

$$\bar{S}_{TOT} - \bar{S}_{ee} = \bar{S}_{ei}(\bar{I}_d - \bar{S}_{ii})^{-1}\bar{S}_{ie} \tag{9}$$

It gives rise to a set of four non-linear equations as explicitly written in (10):

$$\begin{cases} S_{TOT1,1} - S_{FL1,1} + \frac{S_{FL1,2} S_{FL2,1} S_{DUT1,1} + S_{FL1,2} S_{FL2,1} S_{FR1,1} (S_{DUT1,2} S_{DUT2,1} - S_{DUT1,1} S_{DUT2,2})}{(S_{FR1,1} S_{DUT2,2} + S_{FL2,2} S_{DUT1,1} + S_{FR1,1} S_{FL2,2} (S_{DUT1,2} S_{DUT2,1} - S_{DUT1,1} S_{DUT2,2}) - 1)} = 0 \\ S_{TOT1,2} + \frac{S_{FL1,2} S_{FR1,2} S_{DUT1,2}}{(S_{FR1,1} S_{DUT2,2} + S_{FL2,2} S_{DUT1,1} + S_{FR1,1} S_{FL2,2} (S_{DUT1,2} S_{DUT2,1} - S_{DUT1,1} S_{DUT2,2}) - 1)} = 0 \\ S_{TOT2,1} + \frac{S_{FL2,1} S_{FR2,1} S_{DUT2,1}}{(S_{FR1,1} S_{DUT2,2} + S_{FL2,2} S_{DUT1,1} + S_{FR1,1} S_{FL2,2} (S_{DUT1,2} S_{DUT2,1} - S_{DUT1,1} S_{DUT2,2}) - 1)} = 0 \\ S_{TOT2,2} - S_{FR2,2} + \frac{S_{FR1,2} S_{FR2,1} S_{DUT2,2} + S_{FR1,2} S_{FR2,1} S_{FL2,2} (S_{DUT1,2} S_{DUT2,1} - S_{DUT1,1} S_{DUT2,2})}{(S_{FR1,1} S_{DUT2,2} + S_{FL2,2} S_{DUT1,1} + S_{FR1,1} S_{FL2,2} (S_{DUT1,2} S_{DUT2,1} - S_{DUT1,1} S_{DUT2,2}) - 1)} = 0 \end{cases} \tag{10}$$

The system of equations in (10) involves the variables  $S_{DUT1,1}$ ,  $S_{DUT1,2}$ ,  $S_{DUT2,1}$ ,  $S_{DUT2,2}$  that represent the target unknown S-parameters of the DUT that should be computed. For the sake of convenience, the following equalities can be adopted:

$$Y(1) = S_{DUT1,1} \tag{11a}$$

$$Y(2) = S_{DUT1,2} \tag{11b}$$

$$Y(3) = S_{DUT2,1} \tag{11c}$$

$$Y(4) = S_{DUT2,2} \tag{11d}$$

$$C = S_{DUT1,2} S_{DUT2,1} - S_{DUT1,1} S_{DUT2,2} \tag{11e}$$

Using these notations, the system in (10) becomes equal to the non-linear system in (12):

$$\begin{cases} S_{TOT1,1} - S_{FL1,1} + \frac{S_{FL1,2} S_{FL2,1} Y(1) + S_{FL1,2} S_{FL2,1} S_{FR1,1} C}{(S_{FR1,1} Y(4) + S_{FL2,2} Y(1) + S_{FR1,1} S_{FL2,2} C - 1)} = 0 \\ S_{TOT1,2} + \frac{S_{FL1,2} S_{FR1,2} Y(2)}{(S_{FR1,1} Y(4) + S_{FL2,2} Y(1) + S_{FR1,1} S_{FL2,2} C - 1)} = 0 \\ S_{TOT2,1} + \frac{S_{FL2,1} S_{FR2,1} Y(3)}{(S_{FR1,1} Y(4) + S_{FL2,2} Y(1) + S_{FR1,1} S_{FL2,2} C - 1)} = 0 \\ S_{TOT2,2} - S_{FR2,2} + \frac{S_{FR1,2} S_{FR2,1} Y(4) + S_{FR1,2} S_{FR2,1} S_{FL2,2} C}{(S_{FR1,1} Y(4) + S_{FL2,2} Y(1) + S_{FR1,1} S_{FL2,2} C - 1)} = 0 \end{cases} \tag{12}$$

Finally, posing  $SS_1 = S_{TOT1,1} - S_{FL1,1}$  and  $SS_2 = S_{TOT2,2} - S_{FR2,2}$ , (12) can be rewritten as:

$$\begin{bmatrix} SS_1 S_{FL2,2} + S_{FL1,2} S_{FL2,1} & 0 & 0 & SS_1 S_{FR1,1} \\ S_{FL2,2} S_{TOT2,1} & S_{FL1,2} S_{FR1,2} & 0 & S_{FR1,1} S_{TOT2,1} \\ S_{FL2,2} S_{TOT1,2} & 0 & S_{FL2,1} S_{FR2,1} & S_{FR1,1} S_{TOT1,2} \\ SS_2 S_{FL2,2} & 0 & 0 & SS_2 S_{FR1,1} + S_{FR1,2} S_{FR2,1} \end{bmatrix} \begin{bmatrix} Y(1) \\ Y(2) \\ Y(3) \\ Y(4) \end{bmatrix} = \begin{bmatrix} -(SS_1 S_{FR1,1} S_{FL2,2} + S_{FL1,2} S_{FL2,1} S_{FR1,1})C + SS_1 \\ -S_{FR1,1} S_{FL2,2} S_{TOT2,1} C + S_{TOT2,1} \\ -S_{FR1,1} S_{FL2,2} S_{TOT1,2} C + S_{TOT1,2} \\ -(SS_2 S_{FR1,1} S_{FL2,2} + S_{FR1,2} S_{FR2,1} S_{FL2,2})C + SS_2 \end{bmatrix} \tag{13}$$

That in compact form is equivalent to (14):

$$AY = B \tag{14}$$

All components of the solution are linearly dependent on C. The solution of the non-linear equation system in (13) can be achieved by a two step process: first, the linear system (13) can be solved first as function of the parameter C, and then the non-linear equation in (11e) can be solved. By assuming that the matrix A is not singular, the unique solution of the linear system depending on C is given by (15):

$$\bar{Y} = \frac{S_{FL1,2} S_{FR1,2} S_{FL2,1} S_{FR2,1}}{\det(A)} \begin{bmatrix} \left( -\left( S_{FR1,1}^2 S_{FL1,2} S_{FL2,1} S_{TOT2,2} - S_{FR1,1}^2 S_{FL1,2} S_{FL2,1} S_{FR2,2} + S_{FR1,1} S_{FL1,2} S_{FR1,2} S_{FL2,1} S_{FR2,1} \right) C \right) \\ - \left( S_{FL1,1} S_{FR1,2} S_{FR2,1} - S_{FR1,2} S_{FR2,1} S_{TOT1,1} \right) \\ S_{FL2,1} S_{FR2,1} S_{TOT1,2} (S_{FR1,1} S_{FL2,2} C + 1) \\ S_{FL1,2} S_{FR1,2} S_{TOT2,1} (S_{FR1,1} S_{FL2,2} C + 1) \\ \left( -\left( S_{FL1,1}^2 S_{FR1,2} S_{FR2,1} S_{TOT1,1} - S_{FL2,2}^2 S_{FR1,2} S_{FR2,1} S_{FL1,1} + S_{FL1,2} S_{FR1,2} S_{FL2,1} S_{FR2,1} S_{FL2,2} \right) C \right) \\ - \left( S_{FL1,1} S_{FL1,2} S_{FR2,2} - S_{FL1,2} S_{FL2,1} S_{TOT2,2} \right) \end{bmatrix} \tag{15}$$

where  $\det(A)$  is the determinant of the matrix A in (14) given by:

$$\det(A) = S_{FL2,1} S_{FR2,1} S_{FL1,2} S_{FR1,2} \cdot [(SS_1 S_{FL2,2} + S_{FL1,2} S_{FL2,1})(SS_2 S_{FR1,1} + S_{FR1,2} S_{FR2,1}) - SS_1 SS_2 S_{FR1,1} S_{FL2,2}] \tag{16}$$

By replacing the solutions (15) into (11e) a second degree equation is obtained, whose roots are given by:

$$\begin{cases} \lambda_1 = -\frac{1}{S_{FR1,1} S_{FL2,2}} \\ \lambda_2 = \frac{S_{FL1,1} S_{TOT2,2} - S_{FL1,1} S_{FR2,2} + S_{FR2,2} S_{TOT1,1} - S_{TOT1,1} S_{TOT2,2} + S_{TOT1,2} S_{TOT2,1}}{AA} \end{cases} \tag{17}$$

$$AA = (S_{FL1,1} S_{FL2,2} - S_{FL1,2} S_{FL2,1} - S_{FL2,2} S_{TOT1,1}) \cdot (S_{FR1,1} S_{FR2,2} - S_{FR1,2} S_{FR2,1} - S_{FR1,1} S_{TOT2,2}) - S_{FL2,2} S_{FR1,1} S_{TOT1,2} S_{TOT2,1} \tag{18}$$

According to (17) two separate solutions of the non-linear system (12) should be found,  $\bar{Y}_1$  and  $\bar{Y}_2$  coming from  $\lambda_1$  and  $\lambda_2$  in (17), respectively. However, the solution  $Y_1$  is not acceptable since the determinant of the matrix to be inverted in (9) is always zero, as in (19).

$$\det(I - S_{DUT}) = -(S_{FR1,1}Y_1(4) + S_{FL2,2}Y_1(1) + S_{FR1,1}S_{FL2,2}\lambda_1 - 1) = 0 \quad (19)$$

Therefore, the only and unique solution to the problem in (12) is given by  $Y_2$  in (20),

$$\bar{Y}_2 = \frac{1}{AA} \begin{bmatrix} (S_{TOT1,1} - S_{FL1,1})(S_{FR1,2}S_{FR2,1} - S_{FR1,1}S_{FR2,2} + S_{FR1,1}S_{TOT2,2}) - S_{FR1,1}S_{TOT1,2}S_{TOT2,1} \\ S_{FL2,1}S_{FR2,1}S_{TOT1,2} \\ S_{FL1,2}S_{FR1,2}S_{TOT2,1} \\ (S_{TOT2,2} - S_{FR2,2})(S_{FL1,2}S_{FL2,1} - S_{FL2,2}S_{FL1,1} + S_{FL2,2}S_{TOT1,1}) - S_{FL2,2}S_{TOT1,2}S_{TOT2,1} \end{bmatrix} \quad (20)$$

where the scalar variable  $AA$  in the denominator of (20) is given by the expression in (18).  $\bar{Y}_2$  is a vector containing the elements from (11a)–(11d), that is to say, the wanted S-parameters of the DUT that need to be computed at the end of the de-embedding process. Please note as the entries of  $\bar{Y}_2$  are the basic algebraic operations applied to the scattering parameters of the total system (feature-DUT-feature) and of the left and right feature.

#### 4. Validation of the Proposed Single-Step De-Embedding

The single step de-embedding process described in Section 3 is implemented and validated in this Section based on a practical example of a multi-pin connector model suitable for a non-return to zero (NRZ) bit-rate up to 56 Gbps. The NRZ coding is often used for data signaling in telecommunications.

The connector itself, the DUT in Figure 3a,b, is described in [13], developed in the form of a full wave three-dimensional model by SAMTEC [14], and its S-parameters evaluated up to 50 GHz. The S-parameters and the equivalent circuit of the left (FL) and right (FR) fixtures in Figure 3b,c. have been derived in [15]. Based on [15], a single-ended line of the stand-alone connector model is extracted from the multiport S-parameters, and it is embedded in between the corresponding single-ended circuit models of the fixture according to Figure 3a. A more detailed description of the FL and FR fixtures is provided in Figure 3b,c. The S-parameters  $S_{TOT}$  from Port 1 to Port 2 in Figure 3a have been measured in [15] up to 50 GHz. All the four blocks of S-parameters (for the original DUT, for the two fixtures and for the entire system from Port 1 to Port 2) in this work have been considered as input data for this work.

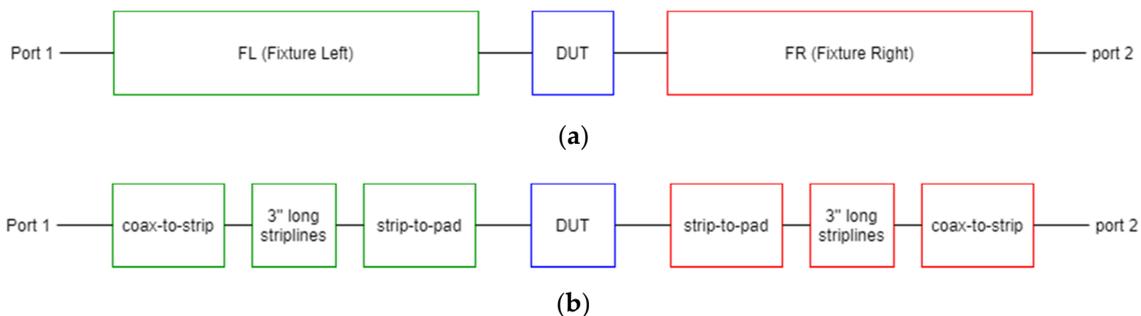
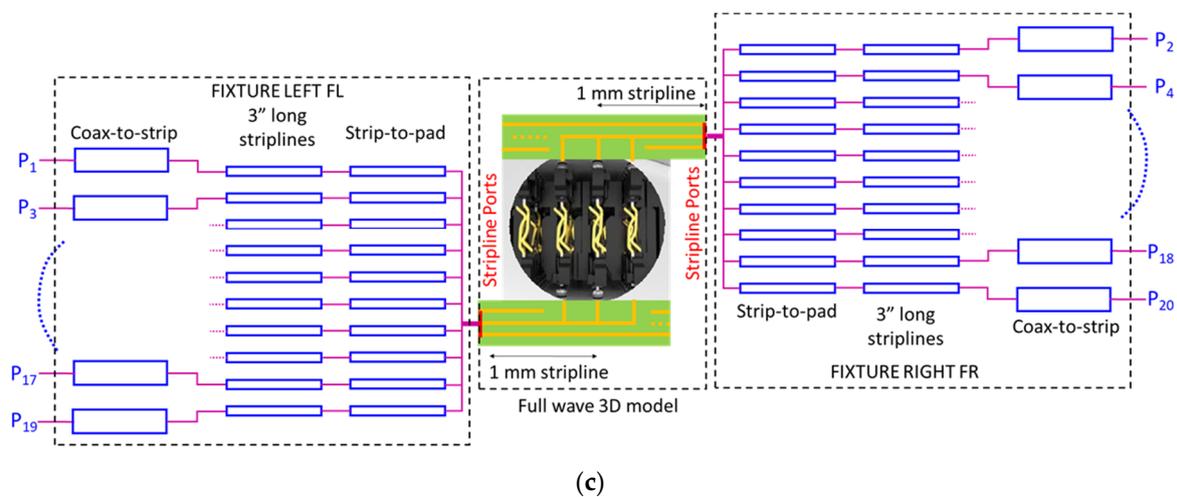


Figure 3. Cont.

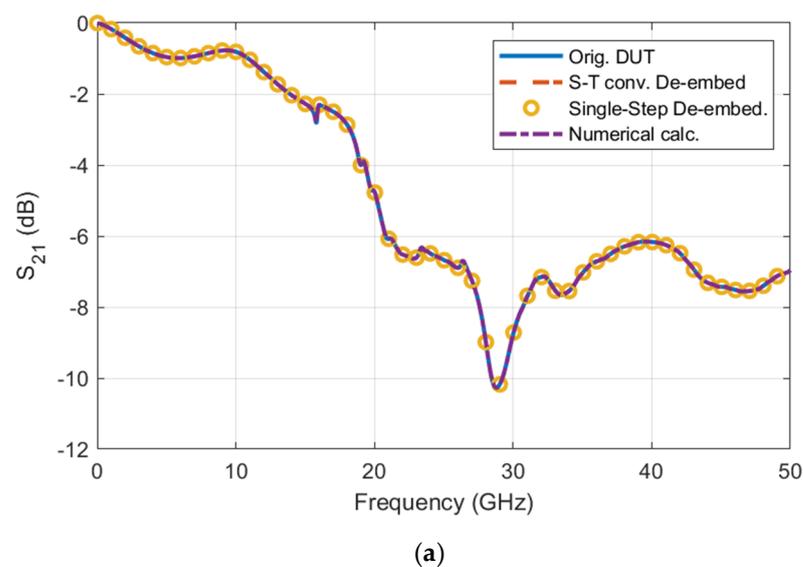


**Figure 3.** (a) Overview of the full de-embedding setup, (b) details of the FL and FR fixtures, (c) complete fixtures from the original multiport model.

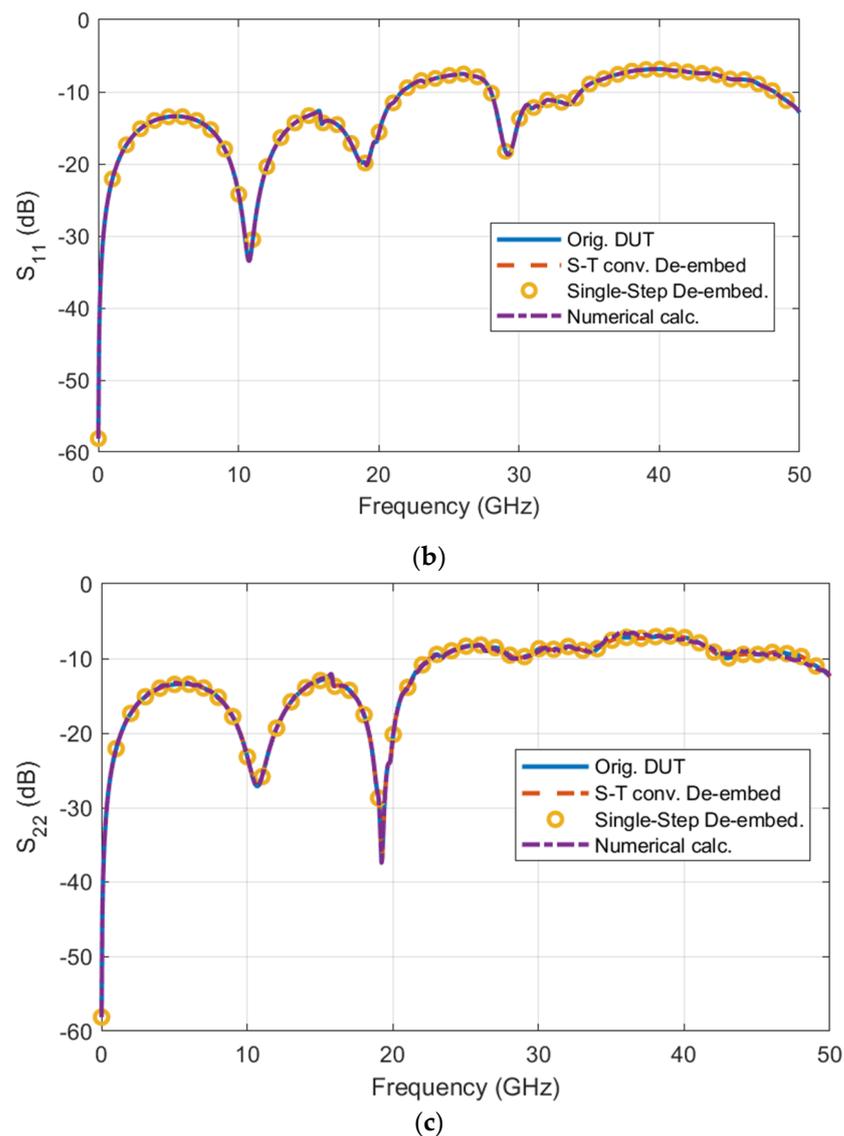
The validation of the single-step procedure consists in finding the S parameters of the DUT (the vector  $Y_2$  from (20)) from the knowledge of  $S_{TOT}$  (from Port 1 to Port 2 in Figure 3a) and comparing them with other independent solutions. The de-embedded DUT S-parameters from (20) (named "Single-Step De-Embed." in Figure 4) are compared:

1. to those of the standalone original DUT known by measurement and modeling as shown in [15] and considered as reference result. They are named "Orig. DUT" in Figure 4;
2. to those obtained by applying the classic standard two-step S-to-T and T-to-S parameter conversion named "S-T conv. De-embedding" in Figure 4;
3. to those computed by applying a numerical iterative solution to the non-linear set of equations in (13) based on the MATLAB built in function *fsolve* [16] named "Numerical calc." in Figure 4.

The overall comparisons of the obtained DUT S-parameters  $S_{21}$ ,  $S_{11}$ , and  $S_{22}$  are reported in Figure 4.



**Figure 4.** Cont.



**Figure 4.** Comparison of the S-parameters obtained by: 1. the standalone original device under test (DUT) considered as reference (continuous line “Orig. DUT”), 2. the classic standard two-steps S-to-T and T-to-S parameter conversion (dashed line “S-T conv. De-embed”), 3. the single-step procedure (circles “Single-Step De-embed.”) and 4. by the numerical iterative solution (dash-dot line “Numerical calc.”). (a)  $S_{21}$ , (b)  $S_{11}$ , (c)  $S_{22}$ .

By starting with a simple visual inspection of Figure 4 it is evident that the accuracy of the solution in (20) is good. In order to better quantify the differences of the S-parameters of the DUT obtained after de-embedding by the different approaches with respect to those of the standalone original DUT considered as reference, Tables 1 and 2 have been compiled. In Table 1 each entry of the Table represents the maximum value of the magnitude of the difference (or maximum absolute error) between the  $S_{ij}$  by each method and the same S-parameter of the standalone original DUT.

In Table 2 each entry of the Table represents the root mean squared difference between the  $S_{ij}$  by each method and the same S-parameter of the standalone original DUT.

The results in Tables 1 and 2 confirm the high level of accuracy of the single-step de-embedding procedure and show some weakness on the direct numerical solution of the non-linear system (13) especially for the  $S_{22}$  parameter whose error, although small, is not acceptable, since it is only one order of magnitude smaller than its absolute value. Both tables also intend to show that, from the accuracy point of view, the single step method

is as accurate as the standard one performed in multiple steps. The main advantages of the new algorithm can be summarized as: (1) it uses a closed form solution without any approximation or inherent numerical error; (2) it is performed in one step so it is about  $3\times$  faster than the classic one: given the normalized execution time of the classic standard de-embedding procedure is 1, the normalized execution time of the single step procedure is 0.333 on the same hardware platform. This latter feature is very important when the de-embedding procedure should be repeated several times, as there are many frequencies of the spectrum of the sought DUT scattering parameters (usually several thousands).

**Table 1.** Maximum absolute error for the de-embedding methods considered.

Method	$S_{11}$	$S_{22}$	$S_{21}$
Classic standard three-step (S-T conv. De-embedding)	$6.15 \times 10^{-9} + j \times 1.08 \times 10^{-8}$	$1.09 \times 10^{-9} + j \times 3.9 \times 10^{-9}$	$2.04 \times 10^{-8} + j \times 3.8 \times 10^{-9}$
Proposed single step (Single-Step De-embed.)	$6.15 \times 10^{-9} + j \times 1.08 \times 10^{-8}$	$1.09 \times 10^{-9} + j \times 3.9 \times 10^{-9}$	$2.04 \times 10^{-8} + j \times 3.8 \times 10^{-9}$
Numerical solution (Numerical calc.)	$1.08 \times 10^{-5} + j \times 1.4 \times 10^{-5}$	$0.03 + j \times 0.025$	$1.30 \times 10^{-4} + j \times 7.33 \times 10^{-4}$

**Table 2.** Root mean squared difference for the considered de-embedding methods.

Method	$S_{11}$	$S_{22}$	$S_{21}$
Classic standard three-step (S-T conv. De-embedding)	$9.26 \times 10^{-18}$	$7.76 \times 10^{-18}$	$2.09 \times 10^{-17}$
Proposed single step (Single-Step De-embed.)	$9.26 \times 10^{-18}$	$7.76 \times 10^{-18}$	$2.09 \times 10^{-17}$
Numerical solution (Numerical calc.)	$3.29 \times 10^{-11}$	$2.20 \times 10^{-4}$	$6.51 \times 10^{-8}$

## 5. Conclusions

A complete closed form method is proposed in this paper to de-embed the side fixtures within the three network fixture-DUT-fixture system typically encountered in the experimental setups for RF and digital device characterization. The method can be readily implemented and provides the DUT S-parameters in a single calculation step, without relying on the S-to-T parameter conversion typical of the classic de-embedding procedure. Furthermore, this standard procedure is applied in three steps, to remove the effect of left and right fixtures separately.

The method is instead a single-step process in which the final results are obtained just calculating the entries of the vector  $\bar{Y}_2$  in (20); it is developed based on the direct solution in closed form of a non-linear system of equations in which it is identified as the valid and unique solution. The proposed de-embedding technique is demonstrated as accurate as the standard de-embedding, more accurate of the numerical solution attempted on the original non-linear system mentioned above, and faster than the standard one. Equation (3) is scalable, and can be extended not only to more blocks but also to blocks with a higher number of input and output ports. This is the aim of future research actions.

The proposed method can be further extended to the more general cases of higher port count rather than the simple two-port network systems.

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